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ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. UTILITY Patent Application

M.S.	O.I.P.E.	PATENT DATE
SCANNED	Q.A.	

APPLICATION NO. 09/896221	CONT/PRIOR	CLASS 709 712	SUBCLASS 32	ART UNIT 2151 2183	EXAMINER Chen, Eddie TSAI
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APPLICANTS
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278

TITLE
System on chip architecture

PTO-2040
12/90

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS		SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
712		32	712	32					
INTERNATIONAL CLASSIFICATION									
6	6	F	9	00					

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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